<u>naaafar nataga</u>

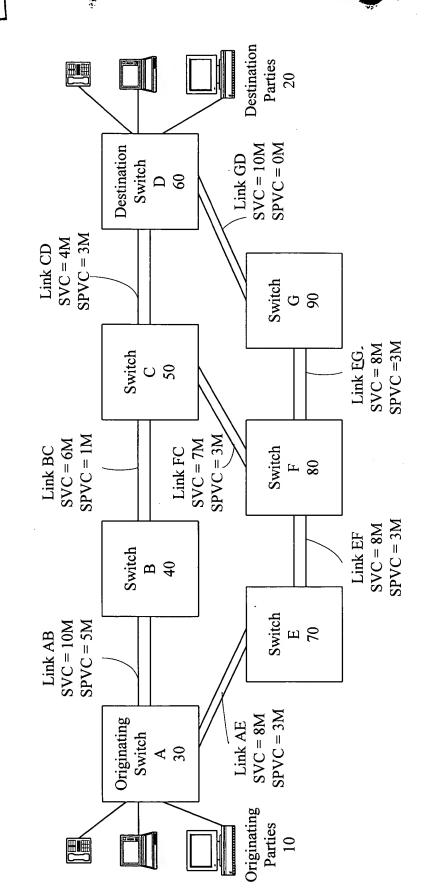


Figure 1.

Processing Module 42 Link Characteristic	Processor 48 Switch B 40
--	--------------------------

Figure 3.

Processing
Module
32

Table
Table
36
Processor 38
Originating Switch A
30

Figure 2.



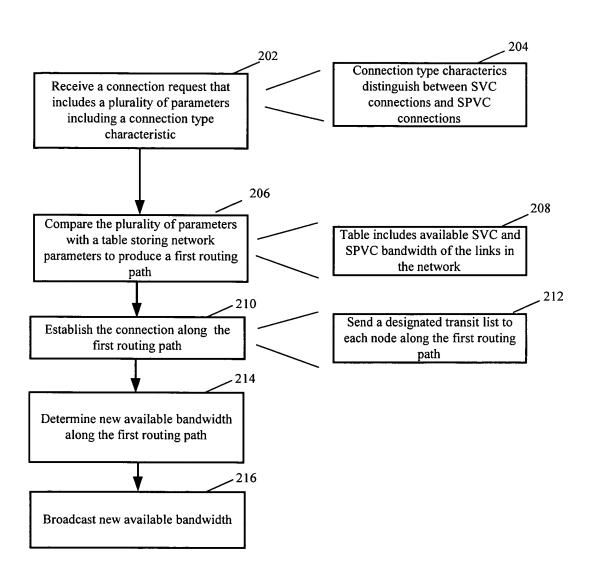


Figure 4.

J

I





4,/4

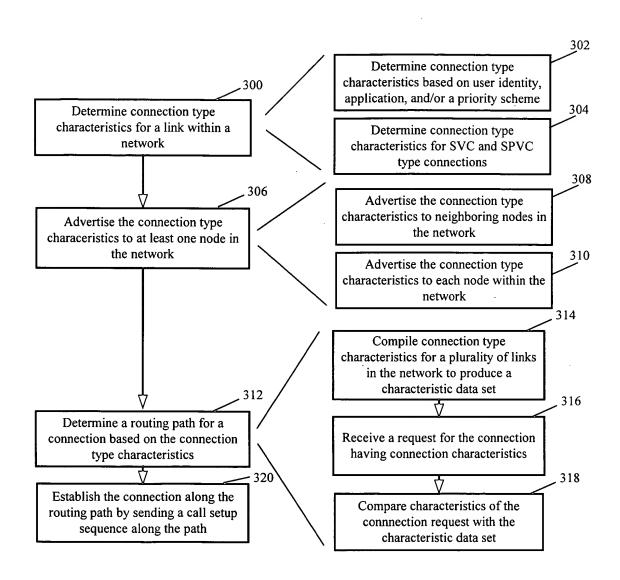


Figure 5.